2133 #s/



Docket No.: 4468-020

**PATENT** 

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

TOMITA, MASAYUKI

Confirmation No. 5837

U.S. Patent Application No. 09/901,150

Group Art Unit: 2133

Filed: July 10, 2001

Examiner: BAKER, Stephen M.

For: BIT El

BIT ERROR MEASUREMENT APPARATUS AND METHOD AND RECORDING

**MEDIUM** 

## **AMENDMENT**

RECEIVED

MAY 0 7 2004

P.O. Box 1450 Alexandria, VA 22313-1450

Commissioner for Patents

**Technology Center 2100** 

Sir:

In response to the Office Action of January 2, 2004, please amend the above-identified application as follows.